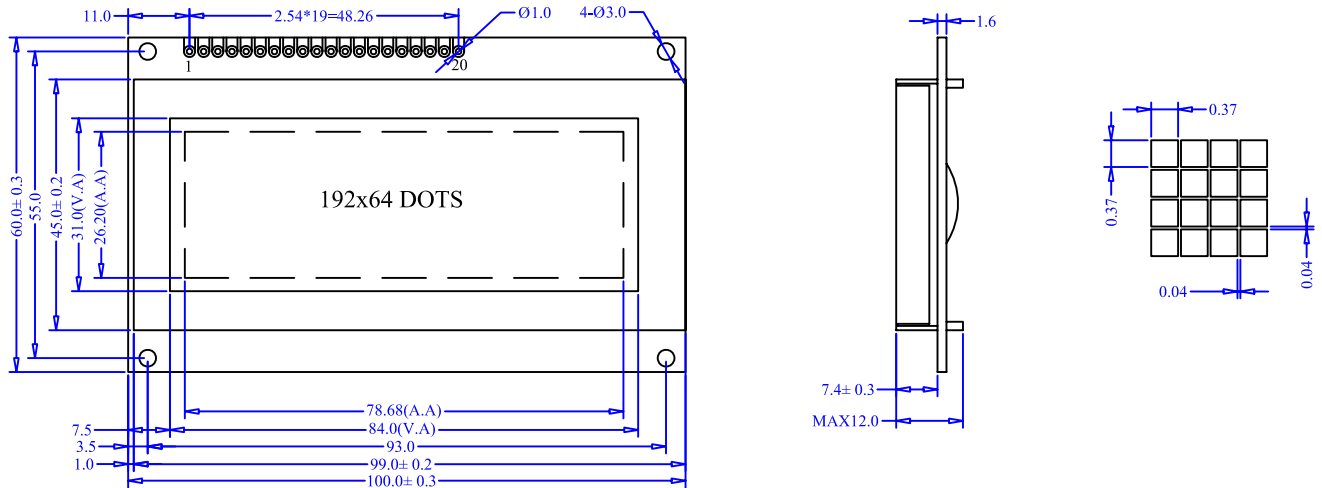


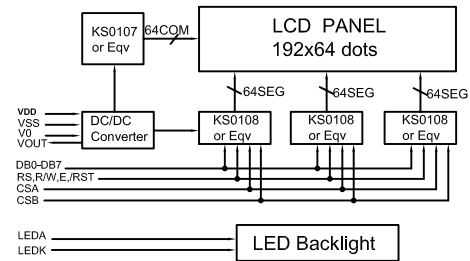
1.DIMENSION OUTLINE



2.MECHANICAL SPECIFICATIONS

ITEM	SPECIFICATIONS	ITEM	REMARK
Module Size(L×W×H)	100.0×60.0×12.0	mm	Reference Dimensional Outline
View Area(W×H)	84.0×31.0	mm	
Effective V/Area	78.68×26.20	mm	
Number of Characters	192×64	-	
Dot Pitch(W×H)	0.41×0.41	mm	
Dot Size(W×H)	0.37×0.37	mm	
Weight (Reflective/Led)	-	g	

3.BLOCK DIAGRAMMECHANICAL



4.LED BACKLIGHT SPECIFICATIONS

ITEM	SYMBOL	TYPE	MAX	UNIT	
Ta=25°C					
①	Forward Voltage	Vf	4.1	4.3	V
	Forward Current	If	180	—	mA
	Emission Wave Length	λp	568	—	nm
②	Forward Voltage	Vf	3.0	3.3	V
	Forward Current	If	60	80	mA
	Emission Wave Length	λp	—	—	nm

5.ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	CONDITION	STANDARD	
			MIN	MAX
Logic Voltage	VDD	Ta=25°C	-0.3V	7V
LCD Voltage	VLCD		-0.3V	17.5V
Input Voltage	VI		-0.3V	VDD+0.3V
Operation Temperature	TOP	—	-20°C	70°C
Storage Temperature	Tst	—	-30°C	80°C

6.INTERFACE PIN CONNECTIONS

ITEM	SYMBOL	LEVEL	FUNCTIONS
1	CSA	H/L	Chip selection Signal 00:Chip 1 01:Chip 2 10:Chip 3
2	CSB	H/L	
3	VSS	0V	Power Ground
4	VDD	+5V	Power supply for logic
5	V0	—	Contrast adjust
6	RS	H/L	H:data L:command
7	R/W	H/L	H:read L:write
8	E	H.H→L	Enable signal
9-16	DB0-DB7	H/L	Data Bus
17	LEDA	+5V	LED B/L Power Supply
18	LEDK	0V	
19	VOUT	—	Output voltage for lcd driving
20	/RST	L	Reset signal

7.ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	MIN	TYPE	MAX	UNIT
Ta=25°C					
Logic Power	VDD	4.5	5	5.5	V
Input High Voltage	VIH	0.8VDD	—	VDD	V
Input Low Voltage	VIL	VSS	—	0.8	V
Output High Voltage	VOH	2.4	—	—	V
Output Low Voltage	VOL	0	—	0.4	V
Logic Current	IDD	—	6	8	mA
Operation Voltage For LCD	VDD-V0	—	12	—	V