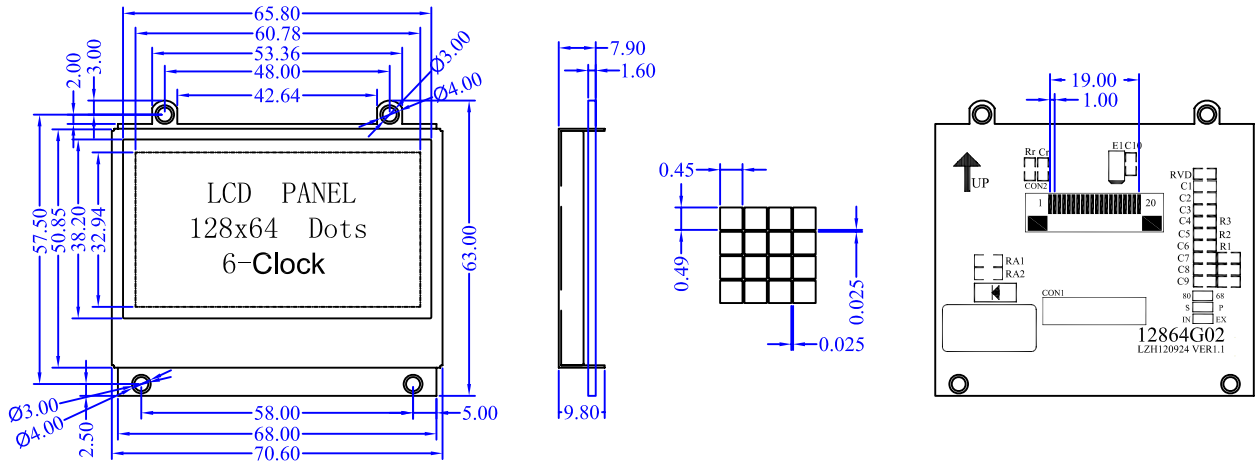


1. DIMENSION OUTLINE



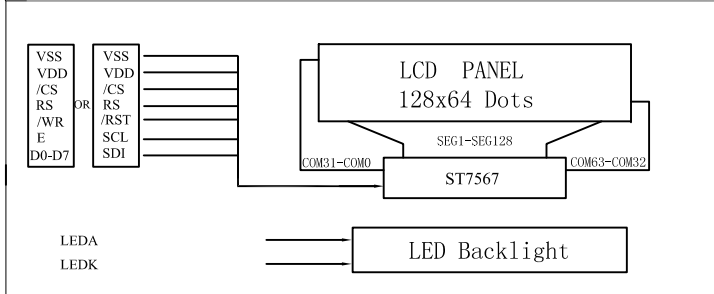
2. MECHANICAL SPECIFICATIONS

ITEM	SPECIFICATIONS	ITEM	REMARK
Module Size(L×W×H)	70.6×63×9.8	mm	Reference Dimensional Outline
View Area(W×L)	65.8×38.2	mm	
Effective V/Area	60.78×32.94	mm	
Number of Characters	128×64	-	
Dot Pitch(W×L)	0.475×0.515	mm	
Dot Size(W×L)	0.45×0.49	mm	

3. ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	CONDITION	STANDARD	
			MIN	MAX
Logic Voltage	V _{DD}	Ta=25°C	0.3V	4.0V
LCD Voltage	V _{LCD}		0.3V	8.5V
Input Voltage	V _I		-0.3V	V _{DD} +0.3V
Operation Temperature	T _{OP}	—	-20°C	70°C
Storage Temperature	T _{St}	—	-30°C	80°C

4. BLOCK DIAGRAM MECHANICAL



5. LED BACKLIGHT SPECIFICATIONS

ITEM	SYMBOL	TYPE	MAX	UNIT
Ta=25°C				
Forward Voltage	V _f	3.0	3.1	V
Forward Current	I _r	45	—	mA
Emission Wave Length	λ _p	White	—	nm

7. ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	MIN	TYPE	MAX	UNIT
Ta=25°C					
Logic Power	V _{DD}	2.7	3.3	3.6	V
Input High Voltage	V _{IH}	0.8V _{DD}	—	V _{DD}	V
Input Low Voltage	V _{IL}	0	—	0.2V _{DD}	V
Output High Voltage	V _{OH}	V _{DD} -0.4	—	V _{DD}	V
Output Low Voltage	V _{OL}	0	—	0.4	V
LCM Current	I _{DD}	—	—	33	mA
Operation Voltage For LCD	V ₀ -V _{SS}	—	10.0	—	V

6. INTERFACE PIN CONNECTIONS

ITEM	SYMBOL	LEVEL	FUNCTIONS(Parallel ,jump=P)
1	CS	L	Chip select signal
2	/RST	L	Reset signal
3	RS	H/L	L:command H:data
4	/RW	L	8080:Write signal 6800: Read/Write
5	RD	H	Rade signal
6-13	DB0-DB7	H/L	Data Bas
14	VDD	3.3V	Power supply for logic
15	VDD	3.3V	
16-18	VSS	0V	Power Ground
19	A	3.3V	Backlight Anode
20	K	0V	Backlight Cathode

ITEM	SYMBOL	LEVEL	FUNCTIONS(Serial ,jump=S)
1	CS	L	Chip select signal
2	/RST	L	Reset signal
3	RS	H/L	L:command H:data
4-11	NC		
12	SCLK	H/L	Clock Input
13	SID	H/L	Data Input
14-15	VDD	3.3V	Power supply for logic
16-18	VSS	0V	Power Ground
19	A	3.3V	Backlight Anode
20	K	0V	Backlight Cathode