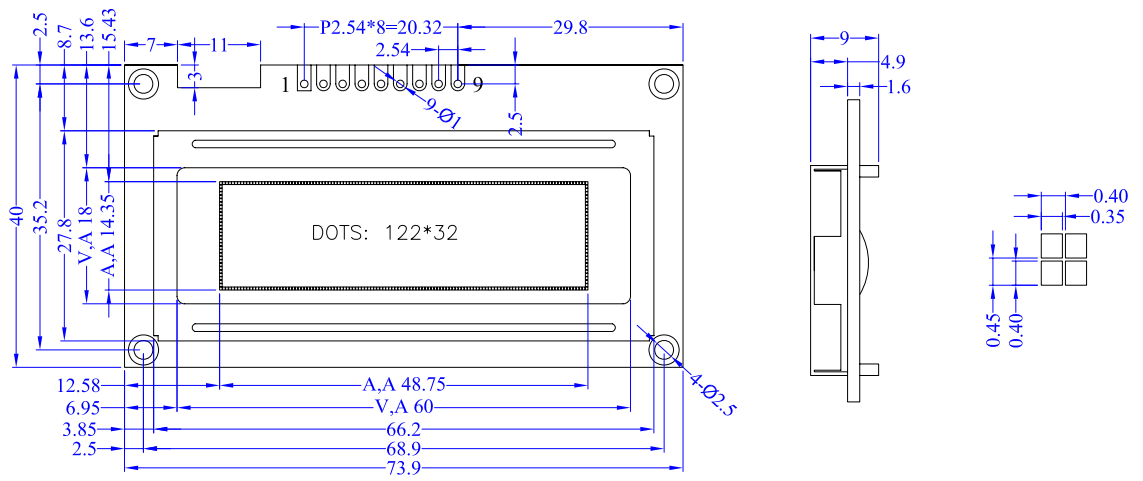
由 Autodesk 教育版产品制作

DJM12232ZE (122x32 DOTS) 1/32DUTY, 1/6BIAS

1.DIMENSION OUTLINE



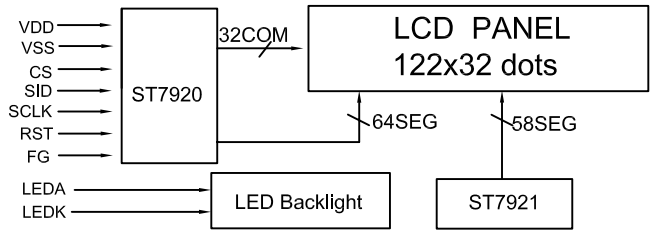
2.MECHANICAL SPECIFICATIONS

ITEM	SPECIFICATIONS	ITEM	REMARK
Modeule Size(L×W×H)	73.9×40×9	mm	Reference Dimensional Outline
View Area(W×H)	60.0×18.0	mm	
Effective V/Area	48.75×14.35	mm	
Number of Characters	122×32	-	
Dot Pitch(W×H)	0.40×0.45	mm	
Dot Size(W×H)	0.35×0.40	mm	
Weight (Reflective/Led)	-	g	

3.ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	CONDITION	STANDARD	
			MIN	MAX
Logic Voltage	V_{DD}	Ta=25℃	-0.3V	7V
LCD Voltage	V_{LCD}		-0.3V	6V
Input Voltage	V_I		-0.3V	V_{DD}+0.3V
Operation Temperature	T_{OP}	—	-20℃	70℃
Storage Temperature	T_{St}	—	-30℃	80℃

4.BLOCK DIAGRAMMECHANICAL



5.LED BACKLIGHT SPECIFICATIONS

ITEM	SYMBOL	TYPE	MAX	UNIT
Ta=25℃				
Forward Voltage	V_f	4.1	4.3	V
Forward Current	I_f	200	—	mA
Emission Vave Length	λ_P	571	—	nm

6.INTERFACE PIN CONNECTIONS

ITEM	SYMBOL	LEVEL	FUNCTIONS
1	VSS	0V	Power Ground
2	VDD	+5V	Power supply for logic
3	CS	H/L	Chip select
4	SID	H/L	Data
5	SCLK	H/L	Clock signal
6	V0/FG	—	Contrast adjust /Frame ground.
7	/RST	H/L	Reset singnal
8	LEDA	+5V	LED B/L Power Supply
9	LEDK	0V	LED B/L Power Supply

7.ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	MIN	TYPE	MAX	UNIT
Ta=25℃					
Logic Power	V_{DD}	4.5	5	5.5	V
Input High Voltage	V_{IH}	0.7V_{DD}	—	V_{DD}	V
Input Low Voltage	V_{IL}	VSS	—	0.6	V
Output High Voltage	V_{OH}	0.8VDD	—	VDD	V
Output Low Voltage	V_{OL}	0	—	0.4	V
Logic Current	I_{DD}	—	3	5	mA
Operation Voltage For LCD	V_{DD}-V_0	—	4.7	—	V

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