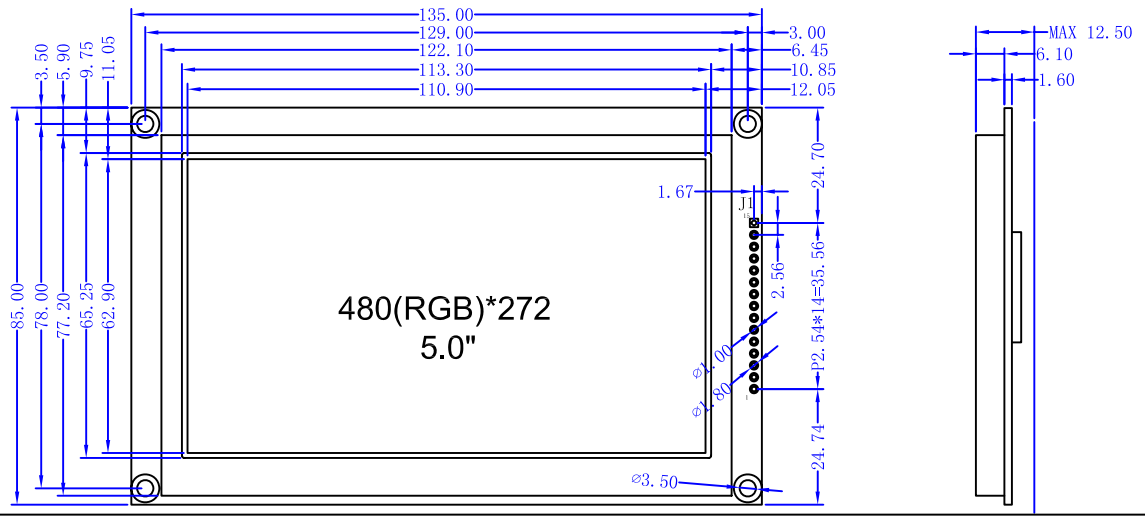
# DJM50B42B01 (480(RGB)X272 DOTS TFT LCD)

## 1.DIMENSION OUTLINE



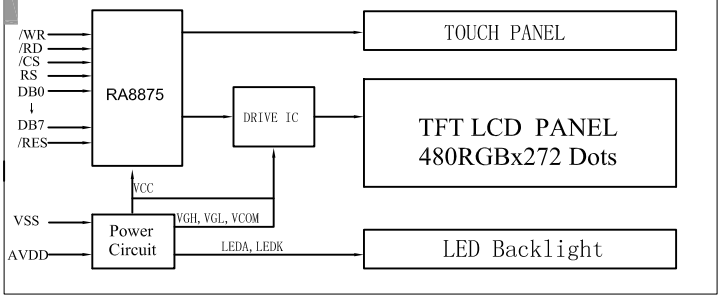

## 2.MECHANICAL SPECIFICATIONS

| ITEM | SPECIFICATIONS | ITEM | REMARK |
|---|---|---|---|
| Module Size(L×W×H) | 135.0×85.0×12.5 | mm | Reference Dimensional Outline |
| View Area(W×H) | 113.3×65.1 | mm | |
| Effective V/Area | 110.9*62.9 | mm | |
| Number of Characters | 480(RGB)×272 | – | |
| Dot Pitch(W×H) | 0.198×0.198 | mm | |
| Dot Size(W×H) | | mm | |

## 3.ABSOLUTE MAXIMUM RATINGS

| ITEM | SYMBOL | CONDITION | STANDARD | |
|---|---|---|---|---|
| | | | MIN | MAX |
| Logic Voltage | Vcc | Ta=25℃ | -0.3V | 5.5V |
| LCD Voltage | $V_{LCD}$ | | -0.3V | 45V |
| Input Voltage | AVDD | | -0.3V | 5.5V |
| Operation Temperature | $T_{OP}$ | – | -20℃ | 70℃ |
| Storage Temperature | Tst | – | –30℃ | 80℃ |

## 4.BLOCK DIAGRAM MECHANICAL




## 5.LED BACKLIGHT SPECIFICATIONS

| ITEM | SYMBOL | TYPE | MAX | UNIT |
|---|---|---|---|---|
| Ta=25℃ | | | | |
| Forward Voltage | $V_f$ | 15 | 16 | V |
| Forward Current | $I_f$ | 350 | – | mA |
| Emission Vave Length | $\lambda_P$ | White | – | nm |

## 7.ELECTRICAL CHARACTERISTICS

| ITEM | SYMBOL | MIN | TYPE | MAX | UNIT |
|---|---|---|---|---|---|
| Ta=25℃ | | | | | |
| Logic Power | VCC | 4.75 | 5.0 | 5.25 | V |
| Input High Voltage | $V_{IH}$ | 0.8VCC | – | VCC | V |
| Input Low Voltage | $V_{IL}$ | 0 | – | 0.2VCC | V |
| Output High Voltage | $V_{OH}$ | VCC–0.4 | – | VDD | V |
| Output Low Voltage | $V_{OL}$ | 0 | – | 0.4 | V |
| Logic Current | $I_{DD}$ | – | – | 300 | mA |
| Operation Voltage For LCD | $V_{GH}-V_{GL}$ | – | 45 | – | V |

## 6.INTERFACE PIN CONNECTIONS

| ITEM(J1) | SYMBOL | LEVEL | FUNCTIONS |
|---|---|---|---|
| 1 | VCC | +5V | Power |
| 2 | GND | 0V | Power Ground |
| 3 | DB7 | H/L | 8-bit DATA |
| 4 | DB6 | H/L | 8-bit DATA |
| 5 | DB5 | H/L | 8-bit DATA |
| 6 | DB4 | H/L | 8-bit DATA |
| 7 | DB3 | H/L | 8-bit DATA |
| 8 | DB2 | H/L | 8-bit DATA |
| 9 | DB1 | H/L | 8-bit DATA |
| 10 | DB0 | H/L | 8-bit DATA |
| 11 | /REST | L | Reset signal |
| 12 | A0 | H/L | H:command L:data |
| 13 | /CS | L | Chip enable signal |
| 14 | /RD | L | Read signal |
| 15 | /WR | L | Write signal |